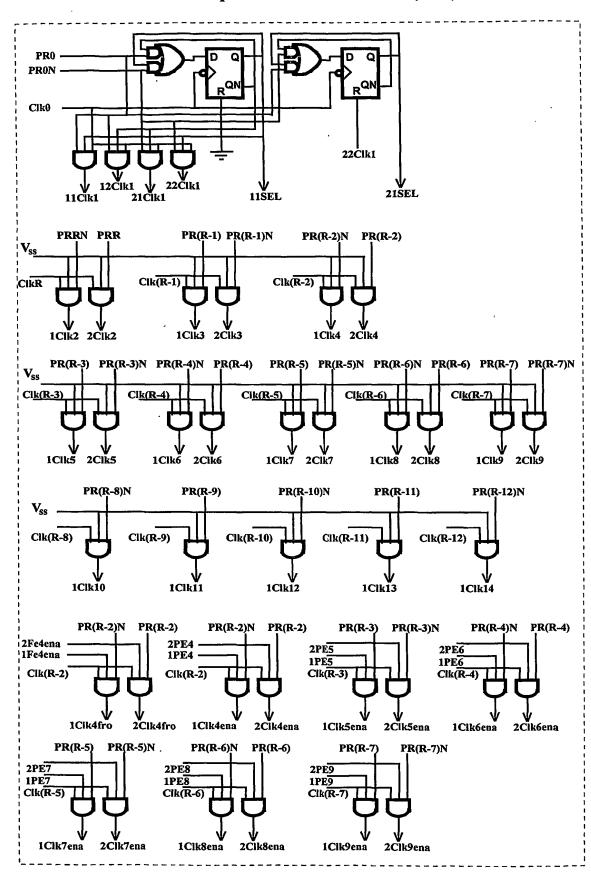


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FIG.2A Sequential Clocks Generation (SCG)



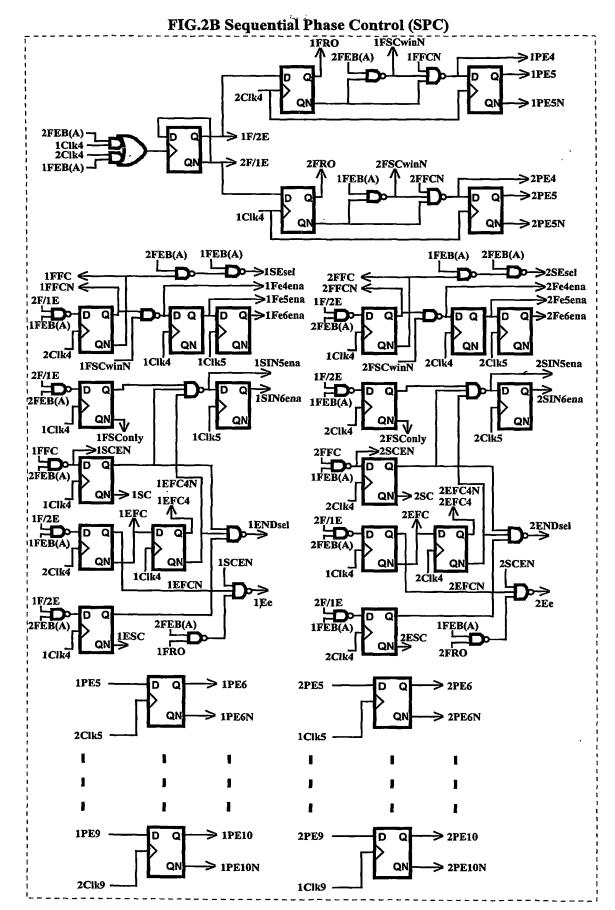
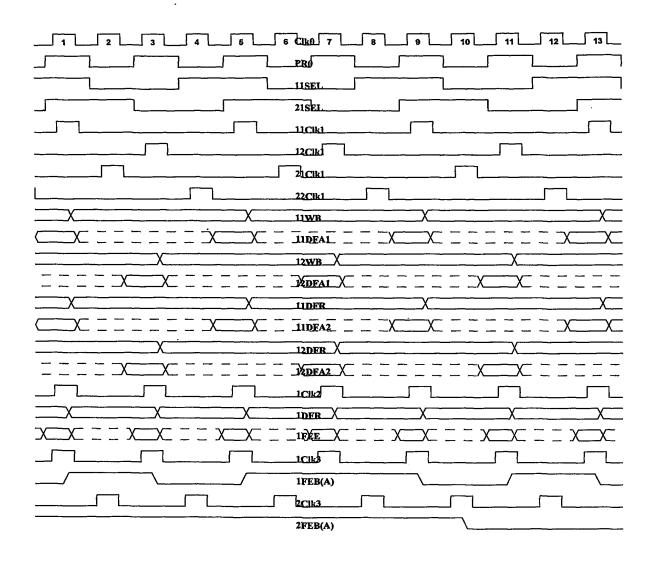
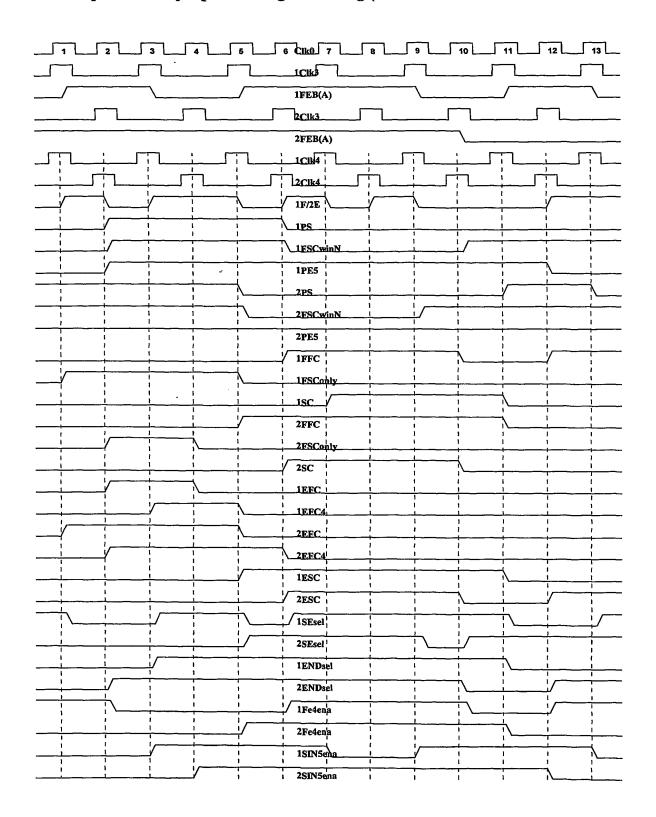


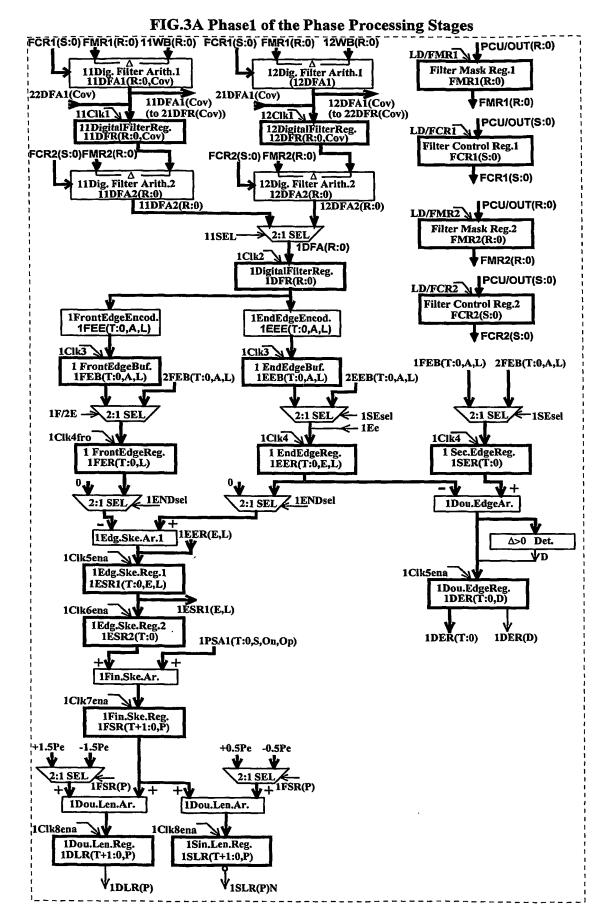
FIG.2C Timing Diagram of the SC and WC



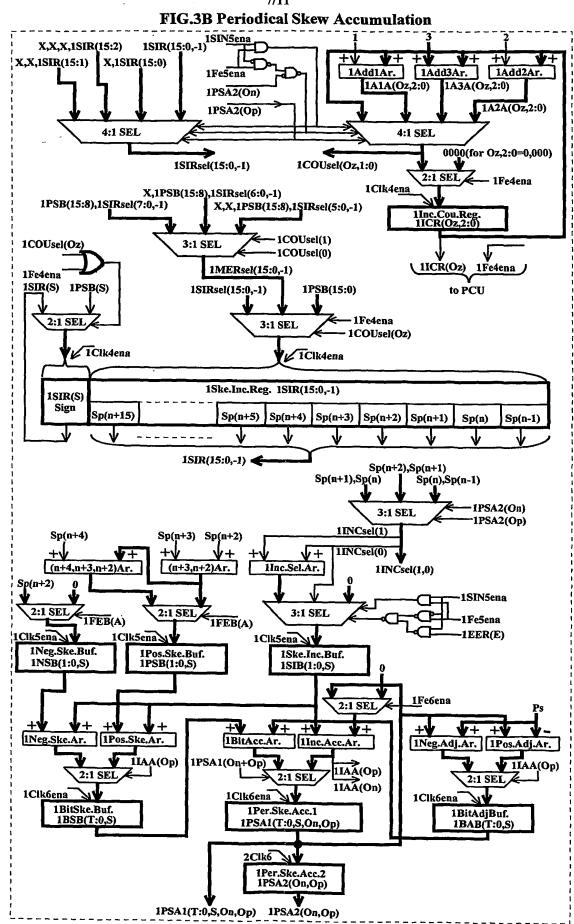
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FIG.2D Timing Diagram of Sequential Phase Control proceeded by a phase2 long data string (continuation of FIG.2C)



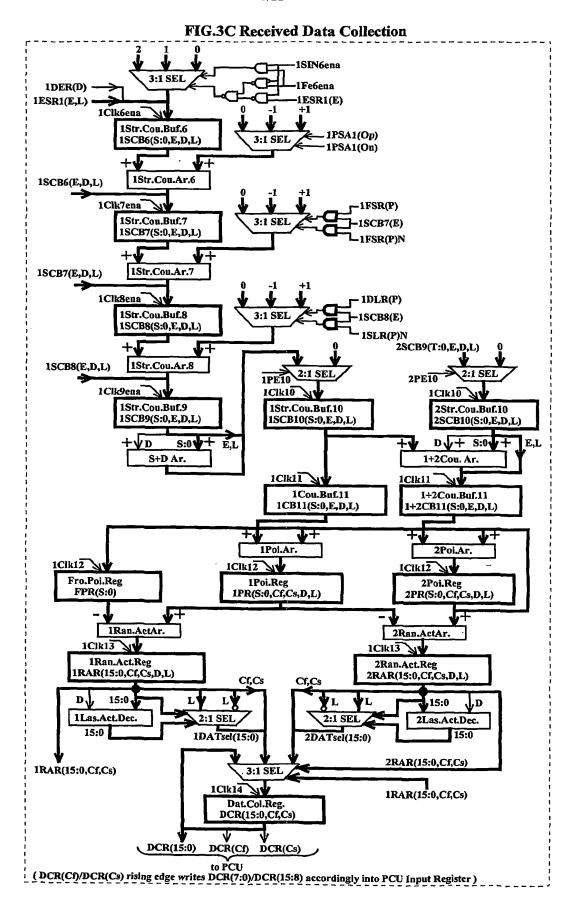


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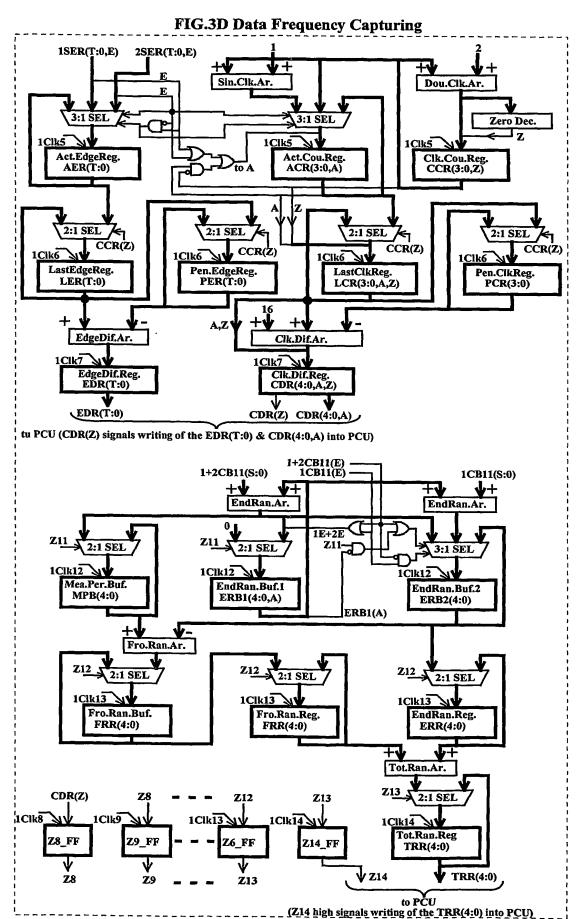
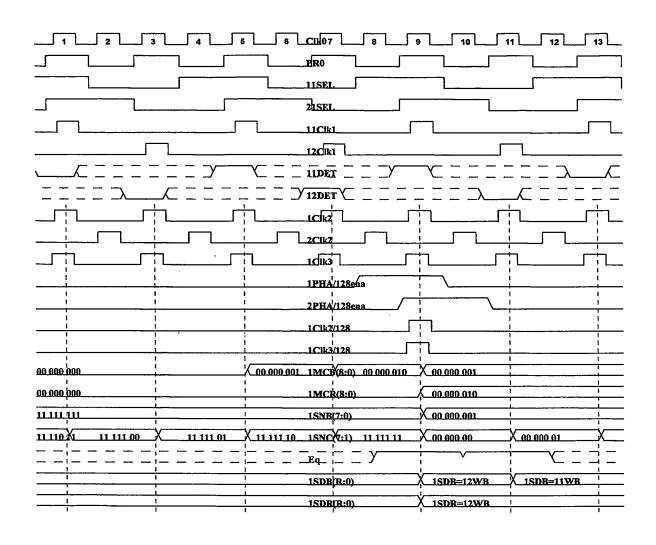


FIG.4A Wave Form Screening & Capturing (WFSC) DCR(P:0) DMR(R:0) 11WB(R:0) DCR(P:0) DMR(R:0) PCU/OUT(R:0) 12WB(R:0) LD/DMR 11MaskDet.Arith. (11MDA) 12MaskDet.Arith. (12MDA) Det. Mask Reg. (DMR) / DMR(R:0) **V11DET** V12DET 11SEL PCU/OUT(P:0) 12DET 11DET Add 1 Ar. 11WB(R:0) LD/DCR 11SEL 12DET 12WB(R:0) 2:1 SEL 0 Det.ControlReg. 11DET (DCR) DCR(P:0) 3:1 SEL 3:1 SEL 1Clk2 1PHA/128ena 1Det.DataBuf. 1MaskCou.Buf. 1DDR(R:0) 1MCB(7:0) MCB>0 DEC. 1Clk3/128 1Clk3/128 1Det.DataReg. 1MaskCou.Reg. 1DDR(R:0) 1MCR(7:0,P) 1DDR(R:0) 🔻 1PHA/128ena √ 1MCR(7:0) V1MCR(P) to PCU (1PHA/128ena&1MCR(P)=1 requests the PCU to read the 1DDR(R:0) and 1MCR(7:0)) PRRN PR(R-1) | | | Clk(R-1) ClkR ERdec 1Clk2 -> Mod. 128 Cou. 1Clk2 QN Zer. Dec. 1Clk2/128 1Clk3/128 1PHA/128ena PR(R-1)N __| Clk(R-1) PRR PCU/OUT(7:0) CikR DQ LD/SNR SampleNr.Reg. 2Clk2 QN (SNR) SNR(7:0) 2PHA/128ena 2Clk2/128 2Člk3/128 1SNC(7:1) 1SNB(7:1) 11WB(R:0) SNR(7:0) Sam.Num.Ar. Log.Comp. 1SNB(1)N 12WB(R:0) 1SNB(0)N Eq 2:1 SEL 3:1 SEL (1SNC = 1SNB)1PHA/128ena 1Clk2/128 1Sam.Num.Buf. 1Sam.Num.Cou. 1Sam.DataBuf. 1SNB(7:0) 1SNC(7:1) 1SDB(R:0) 1Clk3/128 1SNB(7:0) 1Sam.DataReg. 1SDR(R:0) 1PHA/128ena 1SDR(R:0) to PCU (1PHA/128ena=1 signals to PCU, that the requested sample is available to be read)

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FIG.4B Timing Diagrams of the WFSC



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